



21st Century COE, Nanoelectronics for Tera-bit Information Processing

Hiroshima University

The Fifth Hiroshima International Workshop on Nanoelectronics for Tera-bit Information Processing

Date: 9:00-18:30, Monday, January 29, 2007
9:00-15:45, Tuesday, January 30, 2007
Place: Campus Innovation Center Tokyo, International Conference Room,
3-3-6 Shibaura, Minato-ku, Tokyo, Japan
Fee: Free
Banquet: JPY 5,000
URL: <http://www.rcns.hiroshima-u.ac.jp/21coe/>

Invited Speaker

- Prof. Masataka Hirose, Advanced Industrial Science and Technology
- Prof. Dimitri Antoniadis, Massachusetts Institute of Technology
- Prof. Mitsumasa Koyanagi, Tohoku University
- Prof. Toshiro Hiramoto, University of Tokyo

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Hiroshima University

In cooperation with

IEEE Hiroshima Section

The Institute of Electronics, Information and Communication Engineers,
Chugoku Section

Information Processing Society of Japan Chugoku Branch

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IEEE Tokyo Section

The Japan Society of Applied Physics Chugoku-Shikoku Chapter



Research Center for Nanodevices and Systems, Hiroshima University
Super Clean Room (0.1 μm , class 10)

Information

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