

Ultrarapid Thermal Annealing Induced by DC Arc Discharge Plasma Jet Irradiation

S. Higashi, H. Kaku, T. Okada, T. Yorimoto, H. Murakami and S. Miyazaki,

Graduate School of Advanced Sciences of Matter Hiroshima University, Japan



Outline

1. Background and Objectives

2. Experimental

Generation of Thermal Plasma Jet (TPJ) and Its Application to Ultrarapid Thermal Annealing (URTA)

3. Results and Discussion

Noncontact Temperature Measurement Technique with Millisecond Time Resolution

Crystallization of Amorphous Si Films & Its Application to Thin Film Transistor Fabrication

Formation of Si nanocrystals in SiO_x

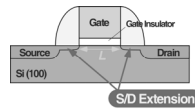
4. Summary



Background & Objectives

Ultrarapid Thermal Annealing (URTA) is one of the key process technologies

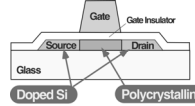
- ULSI Process : Shallow Junction



year / tech. node	Annealing Technology
2007 / 65 nm	1s spike anneal
2010 / 45 nm	1ms FLA (Flash Lamp Anneal)

Temperature Measurement in Milli- and Microsecond Time Domain

- Giant Microelectronics (TFTs, Solar Cells) : Crystallization, Dopant Activation



~10ns Excimer Laser Anneal (ELA)
>Limit in Output Power (~300W)
>Running Cost

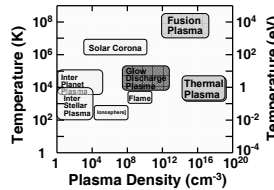
An Alternative High Power Heat Source with Simple Structure

Objectives of This Work are ...

- To Develop a New URTA Technique and a Temperature Measurement Technique with Millisecond Time Resolution
- To Demonstrate the Application of URTA to Electronic Device Fabrication

About Thermal Plasma Jet (TPJ)

Thermal Plasma and Its Advantages as a Heat Source



A comparison of various plasmas in terms of density and temperature.



A photograph of plasma jet generated under atmospheric pressure DC arc discharge.

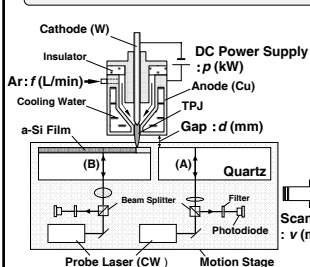
- Thermal Plasma
 - High density
 - Concentration of Electric Power
- Thermal Plasma Jet (TPJ) As a Heat Source ...
 - High power density (Thermal pinch effect) ~100 kW/cm²
 - Simple structure
 - Atmospheric pressure discharge

Low Cost URTA Processing

TPJ is a Very Attractive Heat Source for URTA

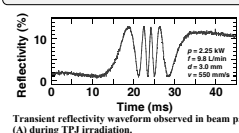
Experimental

Application of TPJ to RTA

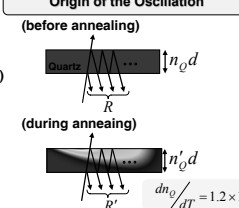


Experimental set up for thermal plasma jet (TPJ) annealing. Beam paths (A) and (B) are used to measure the temperature and phase transformation of a-Si film during anneal.
H. Kaku, et al. Appl. Surf. Sci. 244, (2005) 8.

Oscillation in Transient Reflectivity



Origin of the Oscillation

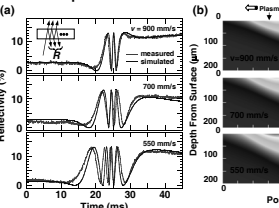
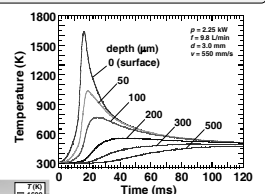


Noncontact Temperature Measurement Technique

Procedure of Analysis

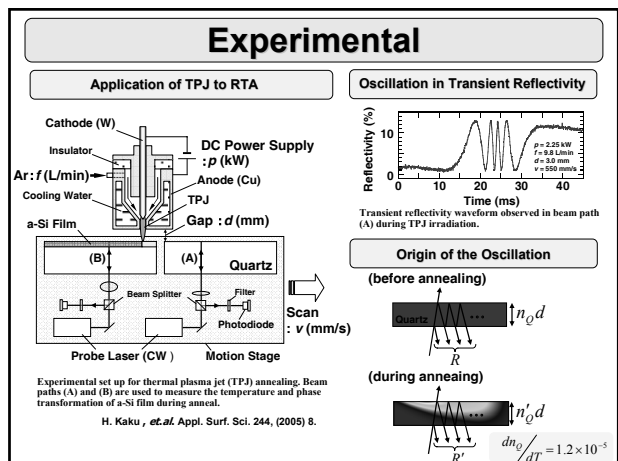
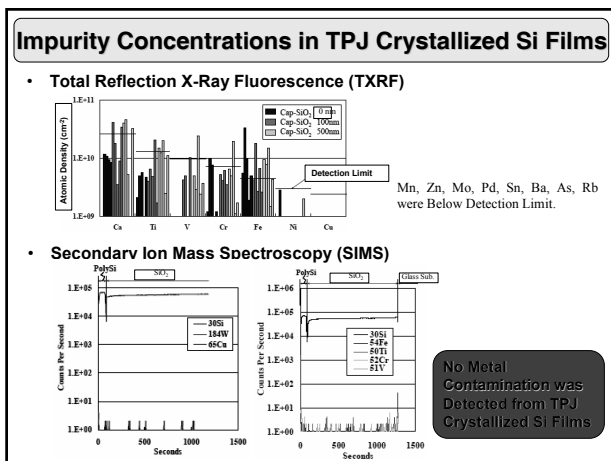
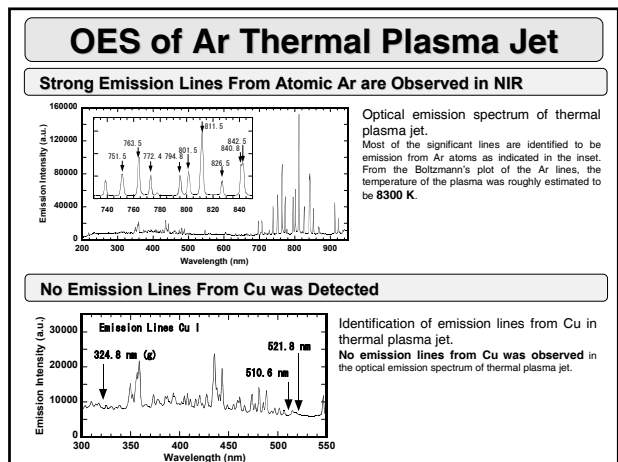
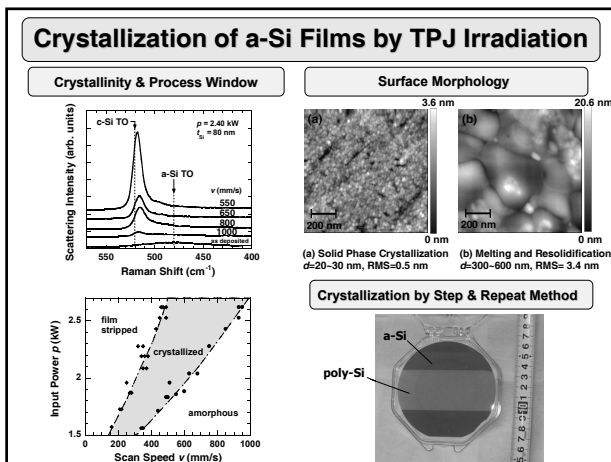
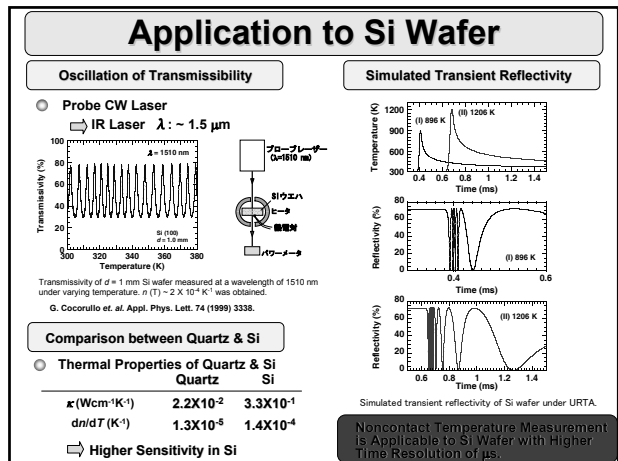
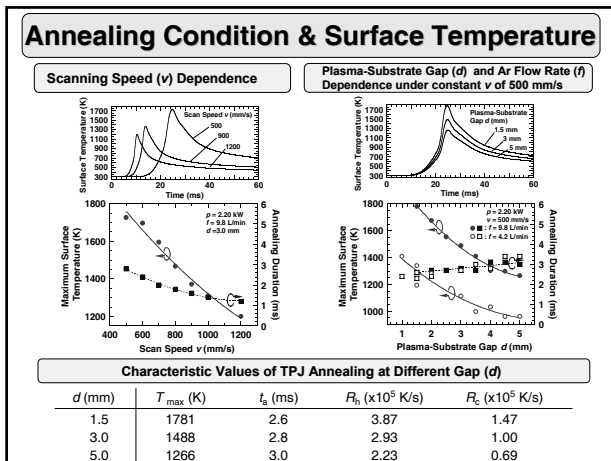
- 2-d Heat Diffusion Simulation
 - Effective power transfer efficiency : η (%)
 - Width of plasma jet : w (mm)
- Optical Simulation
 - $n_0 = 1.5 + 1.2 \times 10^{-5} T(^{\circ}C)$
 - Multiple reflection and interference
- Comparison with Experimental Result

Temporal Variation of Temperature



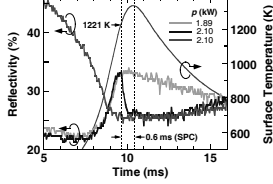
A Noncontact Temperature Measurement Technique with Millisecond Time Resolution has been Successfully Developed

Accuracy < 30 K @-1670K



Crystallization of a-Si Films

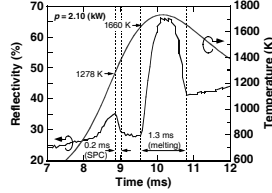
In-situ Observation of Solid Phase Crystallization (SPC)



Annealing conditions:
 $f = 7.0$ L/min, $d = 3.3$ mm, $v = 770$ mm/s

- > Solid Phase Crystallization (SPC)
- > 1220 K, 0.6 ms

In-situ Observation of Melting & Resolidification



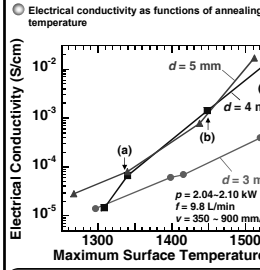
Annealing conditions:
 $f = 7.0$ L/min, $d = 0.8$ mm, $v = 770$ mm/s

- > SPC at 1278 K within 0.2 ms followed by melting and resolidification at 1660 K within 1.3 ms.

Electrical Properties of TPJ Crystallized Si Films

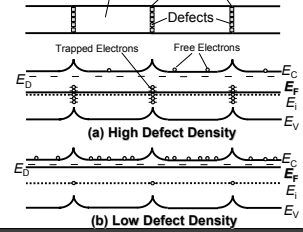
Electrical Conductivity of TPJ Crystallized lightly-doped Si Films

Average P Concentration : $4.3 \times 10^{17} \text{ cm}^{-3}$



- > Carrier Concentration in TPJ Crystallized Si Films Increases with Increasing Annealing Temperature.
- > Defect density in Si Films is Decreases with Annealing Temperature

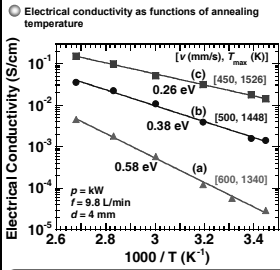
Defects localized at grain boundary work as carrier trap sites.



Electrical Properties of TPJ Crystallized Si Films

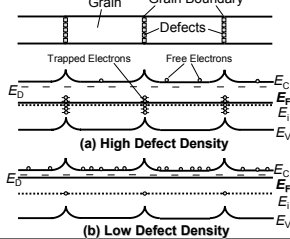
Electrical Conductivity of TPJ Crystallized lightly-doped Si Films

Average P Concentration : $4.3 \times 10^{17} \text{ cm}^{-3}$



- > Carrier Concentration in TPJ Crystallized Si Films Increases with Increasing Annealing Temperature.
- > Defect density in Si Films is Decreases with Annealing Temperature

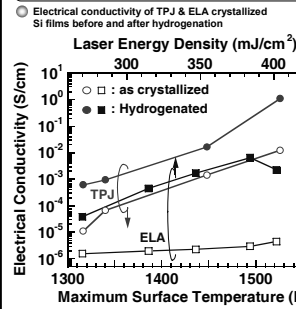
Defects localized at grain boundary work as carrier trap sites.



Electrical Properties of TPJ Crystallized Si Films

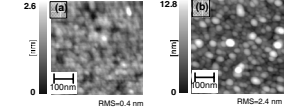
Defect Reduction by Hydrogen Plasma Treatment @ 250C, 60s

Average P Concentration : $4.3 \times 10^{17} \text{ cm}^{-3}$

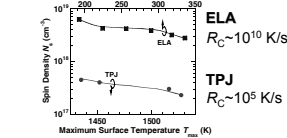


- > TPJ Crystallized Films Have Lower Defect Density Compared to ELA Films
- > Defect Reduction Process Works Efficiently to TPJ Si Films

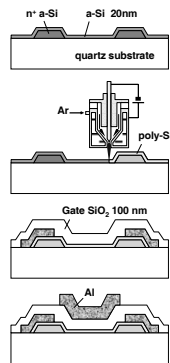
AFM images of (a) TPJ & (b) ELA crystallized Si films



Spin density N_s in (a) TPJ & (b) ELA crystallized Si films

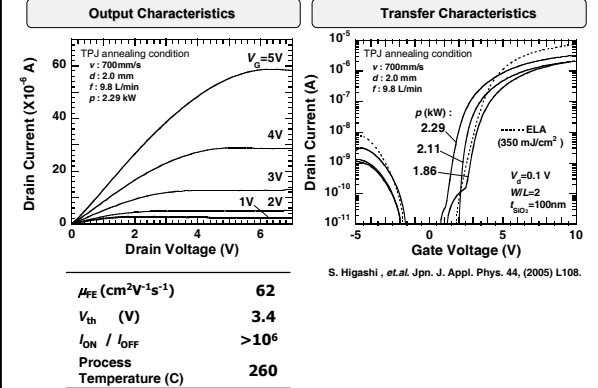


TFT Fabrication Process Flow



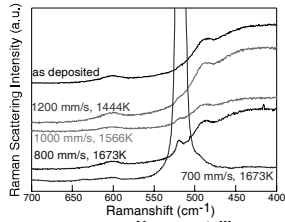
- a-Si Deposition
 - > SiD n+ a-Si (PECVD)
 - > Channel a-Si deposition(PECVD)
- Crystallization
 - > Thermal Plasma Jet
 - > 2.3kW 700mm/s (SPC condition)
 - > Excimer Laser Annealing (ELA) :Reference
- Channel Isolation
 - > Dry Etching (SF₆)
- Source/Drain Electrode Formation
 - > Thermal Evaporation Al
- Gate SiO₂ Formation
 - > SiO Evaporation in Oxygen Radical
- Gate Electrode Formation
 - > Al Evaporation
 - > Defect Reduction
 - > High-Pressure H₂O Vapor Anneal

TFT Performances

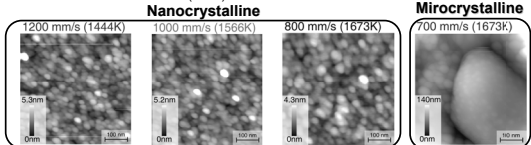
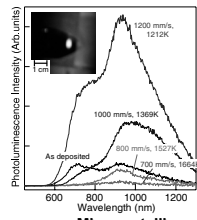


Formation of Nanocrystals in SiO_x

Raman Scattering Spectra of TPJ Annealed SiO_x Films

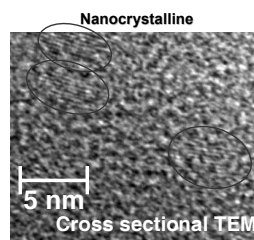


PL Spectra of TPJ Annealed SiO_x Films (excitation : 325 nm)

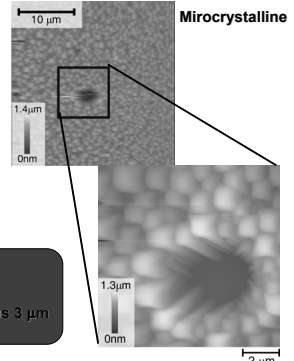


Formation of Nanocrystals in SiO_x

Cross sectional TEM of a TPJ annealed SiO_x film



Large lateral grain growth from SiO_x film with trench structure



- > Si nanocrystals are formed in TPJ annealed SiO_x.
- > Lateral crystalline growth as long as 3 μm is achieved.

Conclusions

1. A New RTA Technique Utilizing Thermal Plasma Jet (TPJ) and Noncontact Temperature Measurement Technique with Millisecond Time Resolution have been Developed.
2. Substrate Surface Temperature is Controlled From 960 to 1781 K with Typical Annealing Duration of 3 ms.
3. Amorphous Si (a-Si) Films are Crystallized Through Solid Phase or Melting & Resolidification Depending on the Annealing Condition.
4. TFTs Fabricated Using TPJ Crystallization Technique Show Good Electrical Performance with μ_{fe} of 62 cm²/Vs and V_{th} of 3.4 V.
5. Nano- and Micrometer Sized Si Crystalline Growth is Achieved by TPJ Annealing of SiO_x Films.